

# Universität Stuttgart

Institut für Robuste Leistungshalbleitersysteme

M.Sc. Laura Manoliu Pfaffenwaldring 32, 70569 Stuttgart Interimsgebäude 1 +49 711 685-61685 Iaura.manoliu@ilh.uni-stuttgart.de

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FPGA implementation of an adaptive, realtime, energy-efficient data rate tuning algorithm

Bachelor/Research/Master Thesis – Bachelor-/Forschung/Master-Arbeit

HF

**FIV** 

## Project:

In the framework of the EIVE Project the Institute of Robust Power Semiconductor Systems (ILH) is developing a 6-Unit CubeSat, flying in the Low Earth Orbit (LEO). Data acquisition algorithms have to be efficiently implemented in Programming Logic (FPGA) and Programming Software (CPU)

## **Scientific Mission:**

- (1) PRBS transmission with various modulation schemes (QPSK, n-QAM, ...) and different data rates for in-depth data-link analysis;
- (2) E-Band Link Budget calculation, considering all the atmospheric effects;
- (3) Download high resolution images stored onboard for Earth observation applications.





www.eive.space



#### Your Tasks:

- Investigate possible flexible datarate changing algorithms for encoding and packetizing the data;
- Implement on the FPGA and on the CPU the selected algorithms and draw the conclusion about the energy efficiency and feasability;
- Adapt the algorithm for different data aquisition sources: BRAM (PRBS sequences for the continiously changing modulation formats) and Flash/RAM for the stored images.

#### Your Qualifications:



- Hands-on experience in developing FPGA algorithms;
- Familiar with software development: VHDL and/or Verilog, Xilinx Vivado;
- Passionate for producing high-quality, space-ready and well-tested code;
- Knowledge of RF circuit design is advantageous;
- Knowledge of communication protocols is an asset;
- Availability for team working is required.



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German description also available at request.

