



IMST GmbH is one of the world's leading service providers in the field of radio development and microelectronics with head office in Kamp-Lintfort. The commitment of more than 170 employees and the quality of our innovative products and services has earned us high recognition - among partners and users.

At the earliest possible date, we are offering

→ **MASTER THESIS (6M)** **INTEGRATED CIRCUIT DESIGN (SIGE - MMIC)**

Duration: 6 months

For our recent product development in the field of space and radiation hard components we are looking for master students with background in integrated circuit design. Our Novelo Chip is a highly integrated Frequency synthesizer integrated with VCOs and RF circuits in a single SiGe-Chip.

We offer:

- Theoretical and practical training in SiGe circuit design, with a focus on the 1.5 to 12-GHz frequency range.
- Great chance to design highly integrated state-of-the-art circuits
- Focus of thesis will be defined after personal meeting
- Competent support of work.
- Use of professional design tools (ADS/Cadence)

Your background:

- Studies in electrical engineering or communications engineering/ information technology with interest or basic skills in integrated circuits / circuit design.
- Depending on your major field, skills in:
 - High-frequency circuit technology
 - High frequency measurement

To be able to process your application as quickly as possible, we need your complete application documents

The following documents are required:

- A current curriculum vitae with photograph
- Job references (if available)
- Latest transcript of your grades

Have we piqued your interest?

Then please send your complete application documents to:

Hardcopy via mail:
IMST GmbH
Carl-Friedrich-Gauss-Str. 2-4
47475 Kamp-Lintfort / Germany

PDF via e-mail:
jobs@imst.de
Reference Nr.: IMST_1812_2
(Attachments only as pdf files please)

